

FIG.1

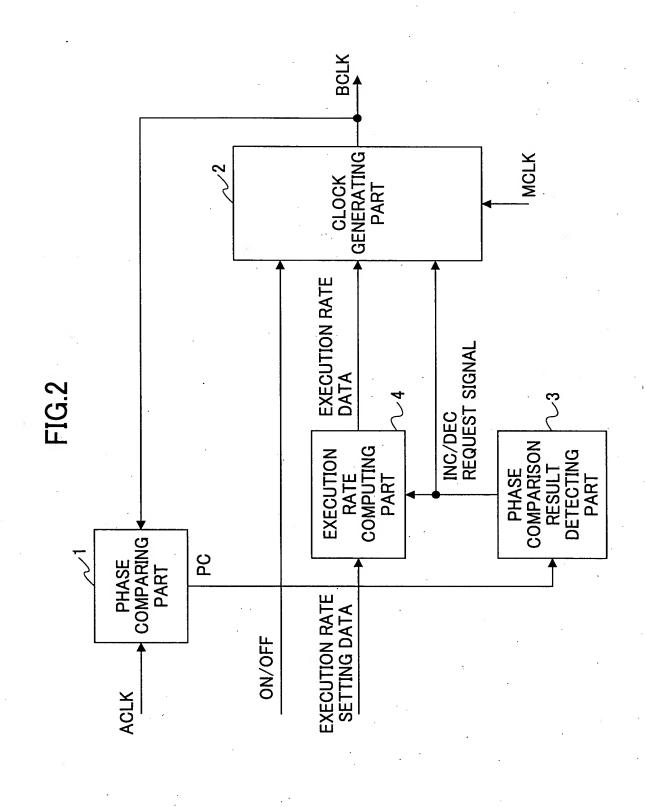
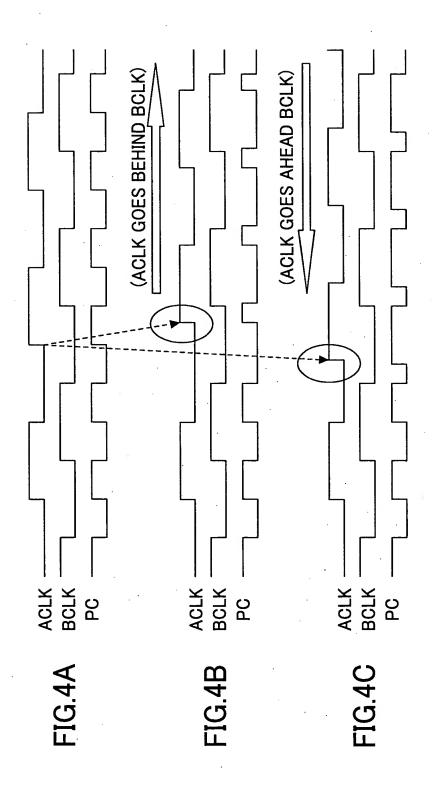


FIG 3



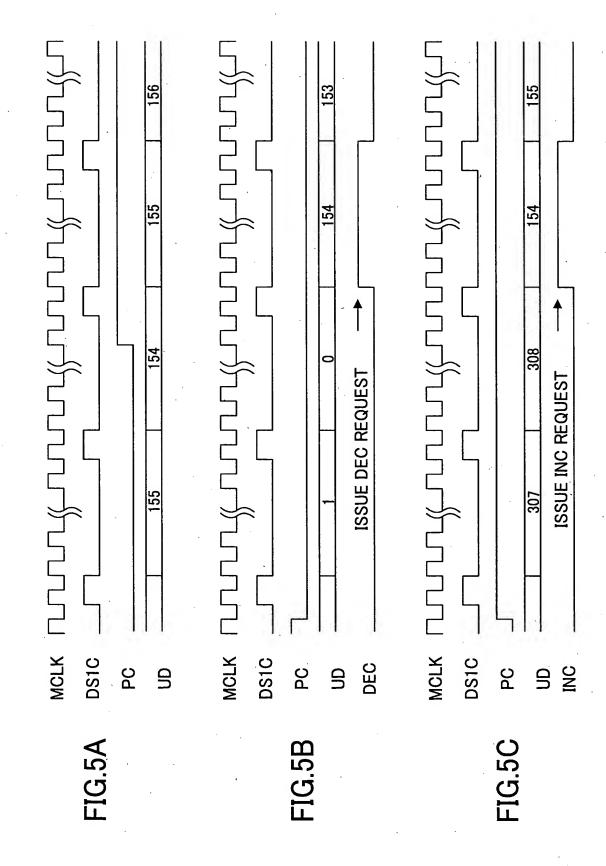
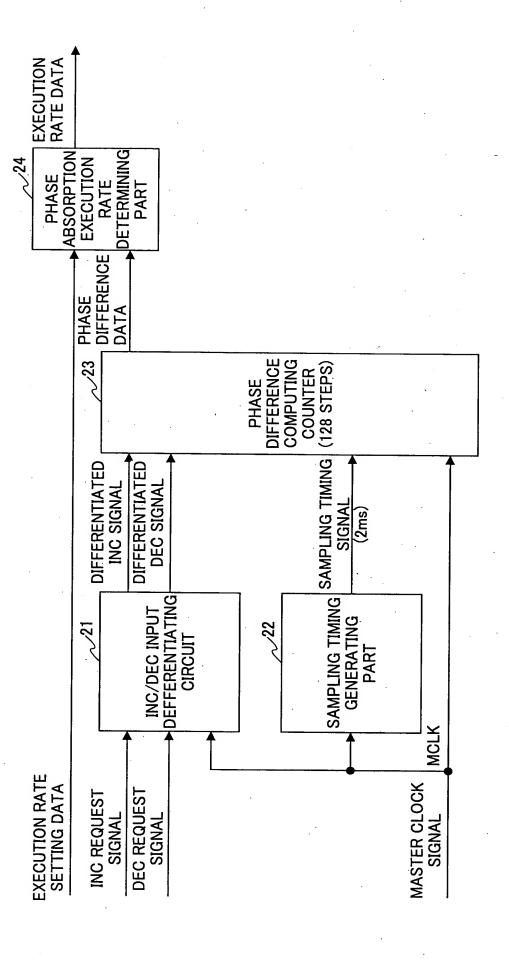
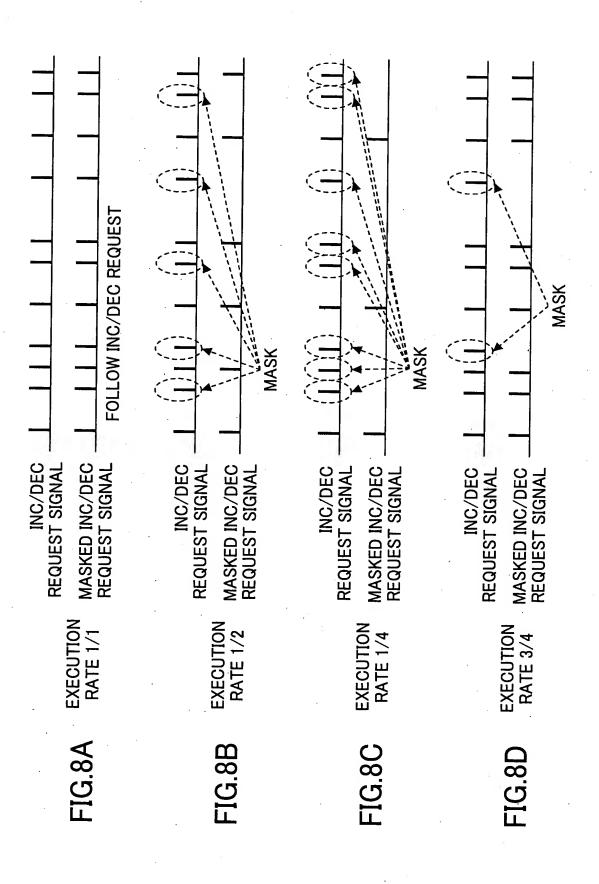
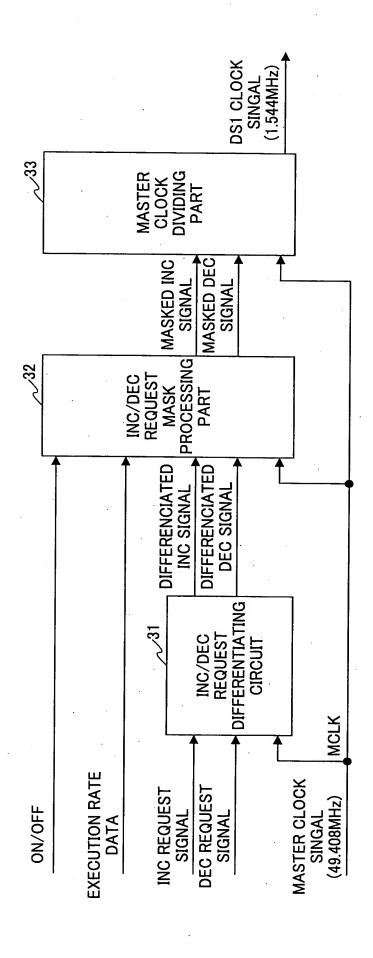


FIG.6



1/1 40bit 1/1 30bit 3/4 20bit 1/2 10bit 1/2 1/1 38bit 1/1 28bit 3/4 19bit 1/2 9bit 1/2 1/1 38bit 1/1 28bit 3/4 18bit 1/2 8bit 1/2 1/1 37bit 1/1 27bit 1/2 16bit 1/2 7bit 1/4 1/1 36bit 1/1 26bit 1/2 16bit 1/2 6bit 1/4 1/1 35bit 3/4 25bit 1/2 15bit 1/2 4bit 1/2 1/1 34bit 3/4 24bit 1/2 14bit 1/2 4bit 1/2 1/1 32bit 3/4 22bit 1/2 12bit 3/4 2bit 1/2 1/1 31bit 3/4 21bit 1/2 12bit 1/2 14bit 1/2	EXE	EXECUTION RATE	PHASE DIFFERENCE	EXECUTION RATE	PHASE DIFFERENCE	EXECUTION RATE	PHASE DIFFERENCE	EXECUTION RATE	PHASE DIFFERENCE	EXECUTION RATE
1/1 29bit 3/4 19bit 1/2 9bit 1/1 28bit 3/4 18bit 1/2 8bit 1/1 27bit 3/4 17bit 1/2 7bit 1/1 26bit 1/2 16bit 1/2 6bit 3/4 25bit 1/2 15bit 1/2 5bit 3/4 24bit 1/2 14bit 1/2 4bit 3/4 23bit 1/2 13bit 3/4 3bit 3/4 22bit 1/2 12bit 3/4 2bit 3/4 21bit 1/2 12bit 3/4 2bit	1/1		40bit	1/1	30bit	3/4	20bit	1/2	10hit	1/9
38bit 1/1 28bit 3/4 18bit 1/2 8bit 37bit 1/1 27bit 3/4 17bit 1/2 7bit 36bit 1/1 26bit 1/2 16bit 1/2 6bit 35bit 3/4 25bit 1/2 14bit 1/2 5bit 34bit 3/4 24bit 1/2 14bit 1/2 4bit 33bit 3/4 23bit 1/2 13bit 3/4 3bit 32bit 3/4 22bit 1/2 12bit 3/4 2bit 31bit 3/4 21bit 1/2 11bit 3/4 2bit	1/1		39bit	1/1	29bit	3/4	19bit	1/2	ahit	1/0
37bit 1/1 27bit 3/4 17bit 1/2 7bit 36bit 1/1 26bit 1/2 16bit 1/2 6bit 34bit 3/4 25bit 1/2 14bit 1/2 5bit 34bit 3/4 24bit 1/2 14bit 1/2 4bit 33bit 3/4 22bit 1/2 12bit 3/4 2bit 31bit 3/4 22bit 1/2 12bit 3/4 2bit 31bit 3/4 21bit 1/2 11bit 3/4 1bit	1/1		38bit	1/1	28bit	3/4	18bit	1/2	Shit	1/2
36bit 1/1 26bit 1/2 16bit 1/2 6bit 35bit 3/4 25bit 1/2 15bit 1/2 5bit 34bit 3/4 24bit 1/2 14bit 1/2 4bit 33bit 3/4 23bit 1/2 13bit 3/4 3bit 32bit 3/4 22bit 1/2 12bit 3/4 2bit 31bit 3/4 21bit 1/2 11bit 3/4 1bit	7		37bit	1/1	27bit	3/4	17bit	1/2	7bit	1/2
35bit 3/4 25bit 1/2 15bit 1/2 5bit 34bit 3/4 24bit 1/2 14bit 1/2 4bit 33bit 3/4 23bit 1/2 13bit 3/4 3bit 32bit 3/4 22bit 1/2 1/2 12bit 3/4 2bit 31bit 3/4 21bit 1/2 11bit 3/4 1bit	\geq	1	36bit	1/1	26bit	1/2	16bit	1/2		1/4
3/4 24bit 1/2 14bit 1/2 4bit 3/4 23bit 1/2 13bit 3/4 3bit 3/4 22bit 1/2 12bit 3/4 2bit 3/4 21bit 1/2 11bit 3/4 1bit	1	1	35bit	3/4	25bit	1/2	15bit	1/2	5hi+	1/9
3/4 23bit 1/2 13bit 3/4 3bit 3/4 22bit 1/2 12bit 3/4 2bit 3/4 21bit 1/2 11bit 3/4 1bit	\geq	_	34bit	3/4	24bit	1/2	14bit	1/2	4hit	1/2
3/4 22bit 1/2 12bit 3/4 2bit 3/4 21bit 1/2 11bit 3/4 1bit	//	_	33bit	3/4	23bit	1/2	13bit	3/4	3hit	1/2
3/4 21bit 1/2 11bit 3/4 1bit		_	32bit	3/4	22bit	1/2	12bit	3/4	Shit	1/2
	\sim	1	31bit	3/4	21bit	1/2	11bit	3/4	1hi+	1/2





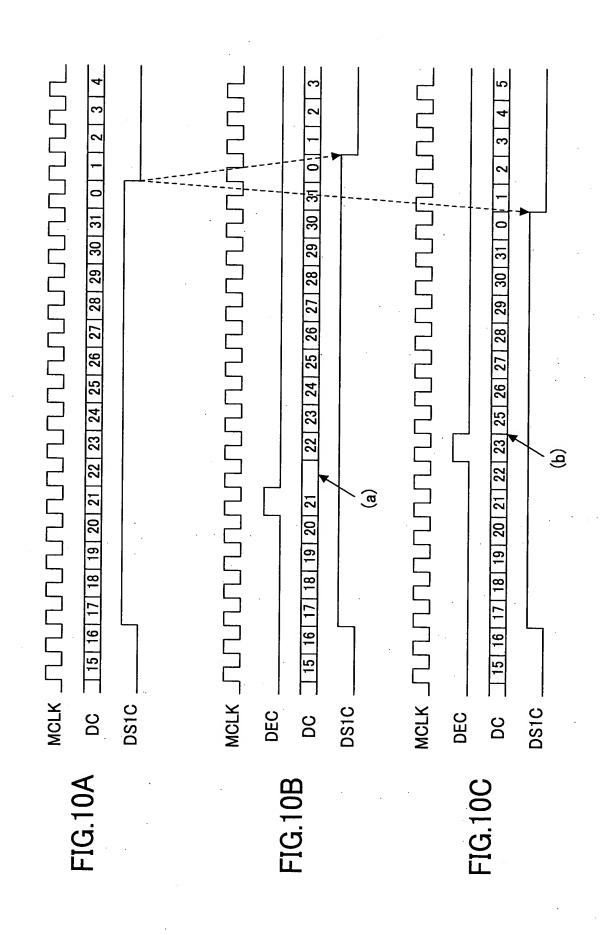


FIG.11A

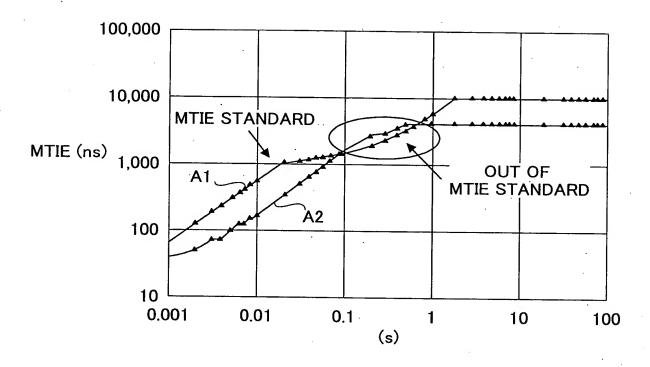


FIG.11B

